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## **AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

## **LISTING OF CLAIMS:**

Claims 1-7 (canceled).

Claim 8 (currently amended): A monolithic ceramic electronic component comprising:

a first element portion including a laminate of ceramic layers and internal electrodes; and

a second element portion including a laminate of ceramic layers and internal electrodes; wherein

at least the first element portion and the second element portion are stacked to define a ceramic laminate, and a porosity of the ceramic layers of the first element portion is different from a porosity of the ceramic layers of the second element portion; and

the first element portion includes a first coil defined by the internal electrodes thereof being electrically connected together, the second element portion includes a second coil defined by the internal electrodes thereof being electrically connected together, and the first coil and the second coil are electrically connected to define an inductor.

Claim 9 (canceled).

Claim 10 (previously presented): The monolithic ceramic electronic component according to Claim 8, wherein the first element portion includes a coil defined by the internal electrodes thereof being electrically connected together, the second element portion includes a capacitor in which any two adjacent electrodes are separated by a

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ceramic layer, the porosity of the ceramic layers of the second element portion is lower than the porosity of the ceramic layers of the first element portion, and the coil and the capacitor are electrically connected to define an LC filter.

Claim 11 (previously presented): The monolithic ceramic electronic component according to Claim 8, wherein the ceramic layers of the first element portion and the ceramic layers of the second element portion are made of the same ceramic material.

Claim 12 (previously presented): The monolithic ceramic electronic component according to Claim 8, wherein the ceramic layers of the first element portion are made of low-permeability ceramic green sheets and the ceramic layers of the second element portion are made of high-permeability ceramic green sheets.

Claim 13 (previously presented): The monolithic ceramic electronic component according to Claim 8, wherein the ceramic layers of the first element portion have a relatively small number of pores and the ceramic layers of the second element portion have a relatively large number of pores.

Claim 14 (previously presented): The monolithic ceramic electronic component according to Claim 8, wherein the first element portion has a high permeability and a high dielectric constant, and the second element portion has a low permeability and a low dielectric constant.

Claim 15 (currently amended): The monolithic ceramic electronic component according to Claim 98, wherein an inductance of the first coil in the first element portion is lower than an inductance of the second coil in the second element portion.

Claim 16 (currently amended): The monolithic ceramic electronic component according to Claim 98, wherein a stray capacitance formed in parallel with the first coil

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of the first element portion is smaller than a stray capacitance formed in parallel with the second coil of the second element portion.

Claim 17 (previously presented): The monolithic ceramic electronic component according to Claim 8, wherein a resonant frequency of the first element portion is higher than a resonant frequency of the second element portion.

Claim 18 (currently amended): The monolithic ceramic electronic component according to Claim 98, wherein a winding direction of the first coil is opposite to that of the second coil.

Claim 19 (previously presented): The monolithic ceramic electronic component according to Claim 8, wherein the porosity of the first element portion is about 30% to about 80% and the porosity of the second element portion is about 10% or less.

Claim 20 (previously presented): The monolithic ceramic electronic component according to Claim 8, wherein the porosity of the first element portion is about 20% and the porosity of the second element portion is about 60%.

Claim 21 (previously presented): The monolithic ceramic electronic component according to Claim 8, wherein the monolithic ceramic electronic component is one of a monolithic inductor, a monolithic impedance component, a monolithic LC filter, a monolithic capacitor, and a monolithic transformer.

Claim 22 (previously presented): A method for making a monolithic ceramic electronic component comprising:

and

stacking ceramic layers and internal electrodes to form a first element portion; stacking ceramic layers and internal electrodes to form a second element portion;

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stacking at least the first element portion and the second element portion to form a ceramic laminate; wherein

an amount of a granular evaporative pore-forming agent incorporated into a ceramic slurry for forming the ceramic layers of the first element portion is different from an amount of the granular evaporative pore-forming agent incorporated into a ceramic slurry for forming the ceramic layers of the second element portion so that the first element portion and the second element portion have different porosities of ceramic layers.

Claim 23 (previously presented): The method according to Claim 22, wherein either the ceramic slurry for forming the ceramic layers of the first element portion or the ceramic slurry for forming the ceramic layers of the second element portion is not incorporated with the granular evaporative pore-forming agent.

Claim 24 (previously presented): The method according to Claim 22, wherein the ceramic slurry for forming the ceramic layers of the first element portion and the ceramic slurry for forming the ceramic layers of the second element portion comprise the same ceramic material.

Claim 25 (previously presented): The method according to Claim 22, wherein the porosity of the first element portion is about 30% to about 80% and the porosity of the second element portion is about 10% or less.

Claim 26 (previously presented): The method according to Claim 22, wherein the porosity of the first element portion is about 20% and the porosity of the second element portion is about 60%.

Claim 27 (previously presented): The method according to Claim 22, wherein the monolithic ceramic electronic component is one of a monolithic inductor, a monolithic

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impedance component, a monolithic LC filter, a monolithic capacitor, and a monolithic transformer.